

Revision Guide for A-Level Computer Science Unit 1.1.2 (SLR 02)

1.1.2 Types of processor

Candidates need to understand the differences between the CISC and RISC processors and the key features and benefits of each.	Before Revision☆☆☆☆☆ After Revision☆☆☆☆☆
Candidates should be aware of the relative benefits of each architecture.	Before Revision☆☆☆☆☆ After Revision☆☆☆☆☆
Candidates need to understand the purpose of GPUs and what applications they are used for (candidates need to understand how GPUs are used to aid graphics, but also other applications for example their use in modeling, data mining, etc.).	Before Revision☆☆☆☆☆ After Revision☆☆☆☆☆
Candidates should understand the benefits and using GPUs and why they are suited to certain tasks (specialist instructions, multiple cores and SIMD processing).	Before Revision☆☆☆☆☆ After Revision☆☆☆☆☆
Candidates need to understand what is meant by a parallel system and the benefits and limitations of parallel processing.	Before Revision☆☆☆☆☆ After Revision☆☆☆☆☆
Candidates need to understand that parallel processing can be achieved through different (i.e. multiple processors in the same computer or distributed or multiple cores in a CPU or GPU).	Before Revision☆☆☆☆☆ After Revision☆☆☆☆☆
Candidates need to understand the benefits of a multicore system in terms of parallel processing and running multiple programs at the same time.	Before Revision☆☆☆☆☆ After Revision☆☆☆☆☆

Slideshow (Shared area)	PG Online Textbook	Teach-ICT	CraignDave (Youtube)
Slides 1 to 41 Read <input type="checkbox"/> Starter Activities <input type="checkbox"/> Viewed links <input type="checkbox"/> Questions <input type="checkbox"/>	Section 1: Chapter 3 Pages 12 to 14 Read <input type="checkbox"/> Questions <input type="checkbox"/> Exercises P15 <input type="checkbox"/>	1.1.2 Types of processor – CISC and RISC Theory <input type="checkbox"/> Lesson Tasks <input type="checkbox"/> 1.1.2 Types of processor – Multicore and parallel processing Theory <input type="checkbox"/> Lesson Tasks <input type="checkbox"/>	OCR A'Level CISC vs RISC Watched <input type="checkbox"/> Cornell Notes <input type="checkbox"/> OCR A'Level GPUs and their uses Watched <input type="checkbox"/> Cornell Notes <input type="checkbox"/> OCR A'Level Multicore and parallel systems Watched <input type="checkbox"/> Cornell Notes <input type="checkbox"/>